

PHYS 231 - Dec. 6, 2023

Today: Binary Numbers & Review.

Reminder: Submit final project lab notebooks by the end of tomorrow's lab.

Ordinary base-10 number system,  
have 10 digits 0, 1, 2, ..., 9.

Consider an arbitrary base-10 number

$$2376901$$
$$= 2 \times 10^6 + 3 \times 10^5 + 7 \times 10^4 + 6 \times 10^3 + 9 \times 10^2 + 0 \times 10^1 + 1 \times 10^0$$

By analogy, the base-2 or binary number system has 2 digits: 0 1

An arbitrary base-2 number might be

1011001

can express this number as

$$1011001_2 \leftarrow \text{base 2}$$

$$1 \times 2^6 + 0 \times 2^5 + 1 \times 2^4 + 1 \times 2^3 + 0 \times 2^2 + 0 \times 2^1 + 1 \times 2^0$$

$$= 64 + 16 + 8 + 1 = 89_{10}$$

↑  
base 10

Convert  $189_{10}$  to base-2 system.

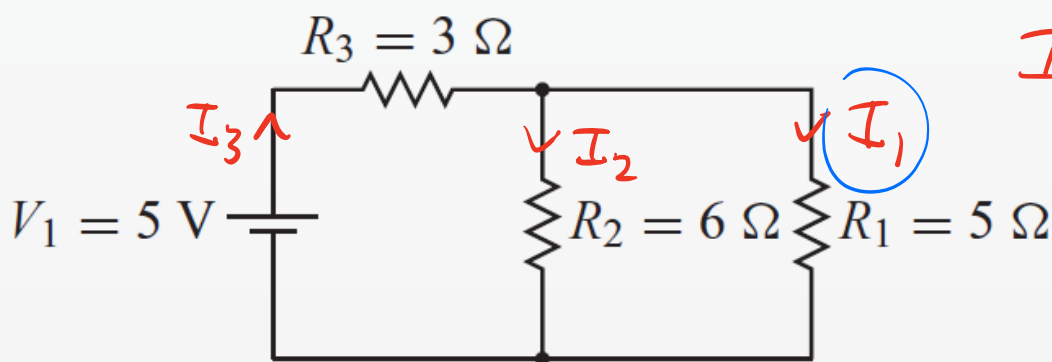
189

$$\left. \begin{array}{l} \{ 2^0 = 1 \\ \times 2^1 = 2 \\ \{ 2^2 = 4 \\ \{ 2^3 = 8 \\ \{ 2^4 = 16 \\ \{ 2^5 = 32 \\ \times 2^6 = 64 \\ \{ 2^7 = 128 \\ \times 2^8 = 256 \end{array} \right\} 192$$

$$\begin{array}{cccccccc} \underline{0} & \underline{1} & \underline{0} & \underline{1} & \underline{1} & \underline{1} & \underline{1} & \underline{0} & \underline{1} \\ 2^8 & 2^7 & \dots & & & & & & 2^0 \end{array}$$

$$10111101_2 = 189_{10}$$

# Eggleston Chapter 1 Problem 5

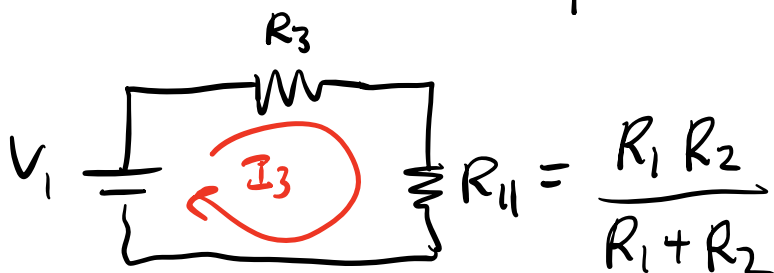


$$I_3 = I_1 + I_2$$

Figure 1.31 Circuit for Problems 4 and 5.

5. Compute the current through  $R_1$  and  ~~$R_2$~~  of Fig. 1.31.

Combine  $R_2$  &  $R_1$  in parallel



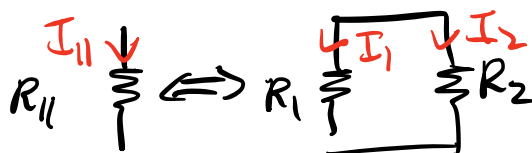
Components in series have same current.

$$I_3 = \frac{V_1}{R_3 + R_{11}}$$

Voltage across  $R_{11}$  is  $I_3 R_{11}$

$$V_{11} = \frac{V_1}{R_3 + R_{11}} R_{11} *$$

For components in parallel, all voltages are the same.



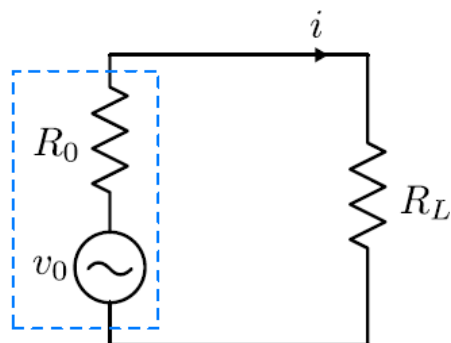
$$\therefore V_{11} = V_{R_1} \text{ (voltage across } R_1 \text{)}.$$

$$\therefore V_{11} = I_1 R_1 \Rightarrow I_1 = \frac{V_{11}}{R_1}$$

$$I_1 = \frac{V_1}{R_1} \frac{R_{11}}{R_3 + R_{11}}$$

# Assignment #2 Problem 2

2. Function generators, like the one you use in the lab, have an internal resistance  $R_0$  in series with the output of the generator. For example, the high-resistance output of the PASCO PI-9587C is  $600 \Omega$ . The figure below shows a schematic of a function generator (inside the blue dashed line) connected across a load resistance  $R_L$ .



(a) For what value of  $R_L$  is the power dissipated by the load resistance a maximum? Express your answer in terms of  $R_0$ . That is,  $R_L = c R_0$ . Find the appropriate numerical value of the constant  $c$ . You must show your work/reasoning to earn full credit.

$$P_L = i^2 R_L \quad i = \frac{V_0}{R_0 + R_L}$$

$$\therefore P_L = V_0^2 \frac{R_L}{(R_0 + R_L)^2} \quad \text{for both } R_L \rightarrow 0 \text{ \& } R_L \rightarrow \infty, \\ P_L \text{ is zero. So, there must} \\ \text{a max. in between } 0 < R_L < \infty$$

To maximize  $P_L$  w.r.t.  $R_L$ , find  $\frac{dP_L}{dR_L} = 0$

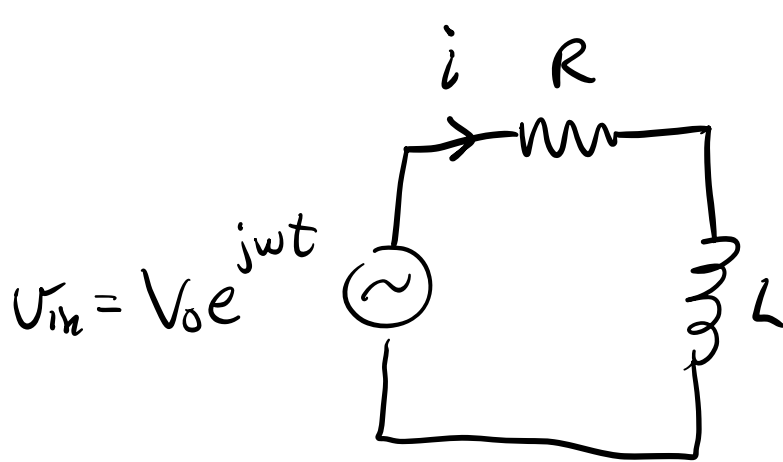
$$\frac{dP_L}{dR_L} = \frac{\cancel{V_0^2}^1}{(\cancel{R_0 + R_L})^2} + \frac{\cancel{V_0^2} R_L (-2)}{(\cancel{R_0 + R_L})^3} = 0$$

$$I = \frac{2R_L}{R_0 + R_L} \Rightarrow \boxed{R_L = R_0}$$

impedance matching  
gives max power  
to  $R_L$ .

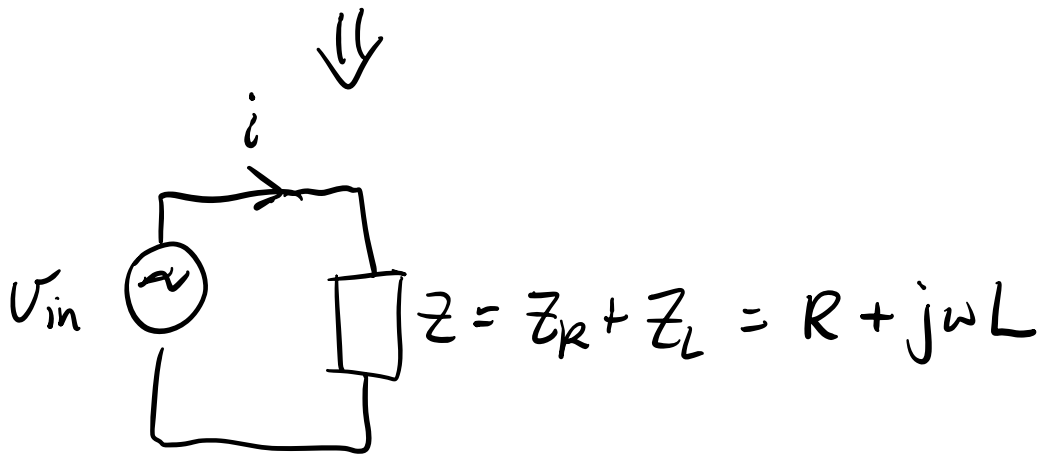
# Assignment #3 Extra Practice

- Consider a resistor  $R$  in series with an inductor  $L$  driven by a function generator that outputs  $v_{in} = V_0 \sin \omega t$ . The current in the circuit is given by  $i = I_0 \sin(\omega t + \phi)$ . Find  $I_0$  and  $\phi$ .



$$i = I_0 e^{j(\omega t + \phi)}$$

find  $I_0$  &  $\phi$ .



Important results:  $I_0 = \frac{V_0}{|Z|}$

$$\tan \phi = - \frac{\text{Im}[Z]}{\text{Re}[Z]}$$

For our circuit  $Z = R + j\omega L$   $\text{Re}[Z] = R$   
 $\text{Im}[Z] = \omega L$

$$\tan \phi = - \frac{\omega L}{R}$$

$$\begin{aligned} |Z|^2 &= Z Z^* \\ &= (R + j\omega L)(R - j\omega L) \\ &= R^2 + (\omega L)^2 \end{aligned}$$

$$|Z| = \sqrt{R^2 + (\omega L)^2}$$

$$I_0 = \frac{V_0}{\sqrt{R^2 + (\omega L)^2}} = \frac{V_0/R}{\sqrt{1 + (\omega L/R)^2}} \quad (\text{low-pass filter})$$

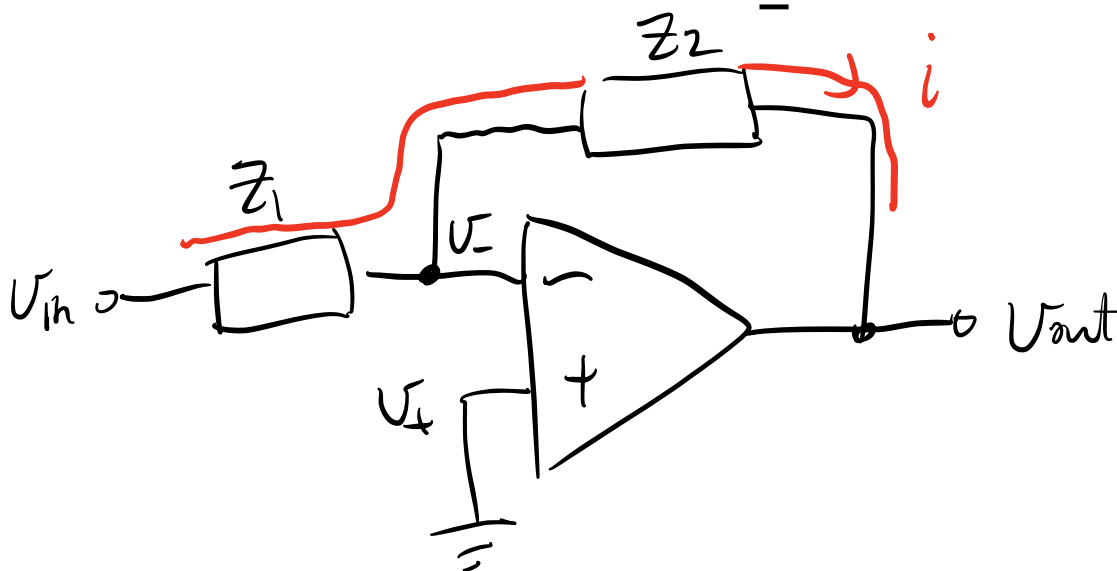
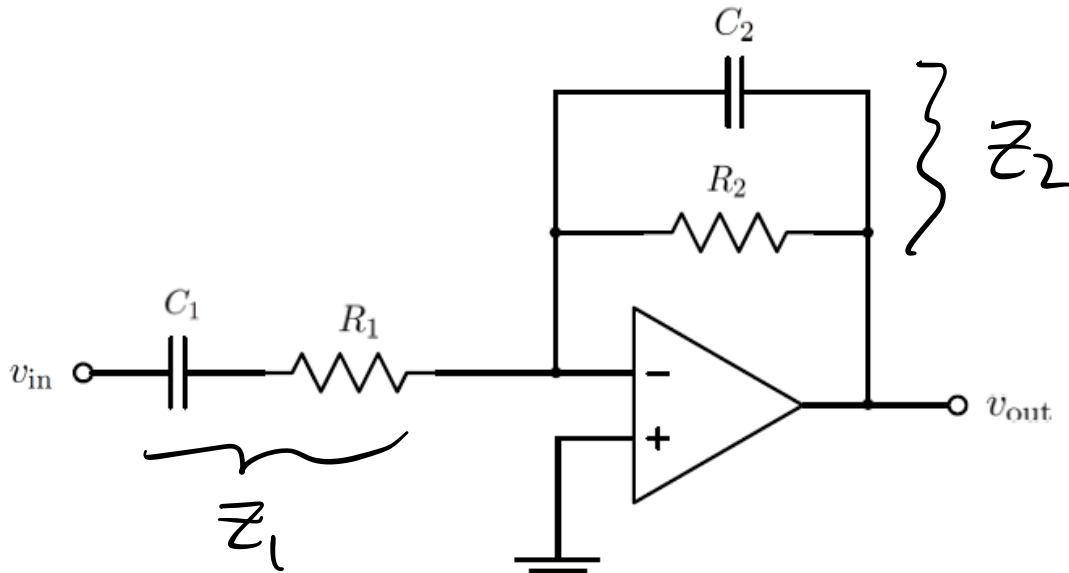


# Assignment #4 Extra Practice

1. The circuit below is a *bandpass* filter/amplifier. Show that:

$$\left| \frac{v_{out}}{v_{in}} \right| = G \frac{\omega R_1 C_1}{\sqrt{1 + (\omega R_1 C_1)^2} \sqrt{1 + (\omega R_2 C_2)^2}}$$

Assume that  $v_{in}$  is a sinusoidal input. When analysing the circuit, make use of the fact that  $Z_C = 1/(j\omega C)$  and  $v_C = iZ_C$ . What is  $G$  in terms of the circuit components?



inverting amplifier  

$$V_{out} = -\frac{Z_2}{Z_1} V_{in}$$

GR #1 : no current into/out of op amp inputs.

GR #2 : w/ neg. feedback, require  $V_+ = V_-$

Here  $U_f = 0 \quad \therefore U_- = 0.$

$$U_{in} - iZ_1 = U_- = 0 \Rightarrow i = \frac{U_{in}}{Z_1}$$

~~$U_- - iZ_2 = U_{out}$~~

$$\therefore U_{out} = -iZ_2$$

$$U_{out} = -U_{in} \frac{Z_2}{Z_1}$$

$$\therefore \left| \frac{U_{out}}{U_{in}} \right| = \left| \frac{Z_2}{Z_1} \right| = \frac{|Z_2|}{|Z_1|}$$

$$Z_1 = R_1 + \frac{1}{j\omega C_1} = \frac{1 + j\omega R_1 C_1}{j\omega C_1} \left( \frac{R_1}{R_1} \right)$$

$$Z_1 = \frac{1 + j\omega R_1 C_1}{j\omega R_1 C_1} R_1$$

$$|Z_1|^2 = Z_1 Z_1^* = \left( R_1 \frac{1 + j\omega R_1 C_1}{j\omega R_1 C_1} \right) \left( R_1 \frac{1 - j\omega R_1 C_1}{-j\omega R_1 C_1} \right)$$

$$|Z_1|^2 = R_1^2 \frac{1 + (\omega R_1 C_1)^2}{(\omega R_1 C_1)^2}$$

$$|Z_1| = R_1 \frac{\sqrt{1 + (\omega R_1 C_1)^2}}{\omega R_1 C_1}$$

$$Z_2 = R_2 \parallel Z_{C_2} = \frac{R_2 Z_{C_2}}{R_2 + Z_{C_2}} = \frac{R_2 \frac{1}{j\omega C_2}}{R_2 + \frac{1}{j\omega C_2}}$$

$$= \frac{R_2}{1 + j\omega R_2 C_2}$$

$$|Z_2| = \sqrt{Z_2 Z_2^*} = \frac{R_2}{\sqrt{1 + (\omega R_2 C_2)^2}}$$

Finally:  $\left| \frac{V_{out}}{V_{in}} \right| = \left| \frac{Z_2}{Z_1} \right| = \frac{|Z_2|}{|Z_1|}$

$$= \frac{R_2}{\sqrt{1 + (\omega R_2 C_2)^2}} \frac{\omega R_1 C_1}{R_1 \sqrt{1 + (\omega R_1 C_1)^2}}$$

$$\therefore \left| \frac{V_{out}}{V_{in}} \right| = \underbrace{\frac{R_2}{R_1}}_{\substack{G \\ \text{gain} \\ \text{factor}}} \underbrace{\frac{\omega R_1 C_1}{\sqrt{1 + (\omega R_1 C_1)^2}}}_{\substack{\text{high-pass} \\ \text{filter}}} \underbrace{\frac{1}{\sqrt{1 + (\omega R_2 C_2)^2}}}_{\substack{\text{low-pass} \\ \text{filter}}}$$

## Eggleston Chapter 8 Problem 2

2. Using only NAND gates, construct a circuit that will implement the following logical expressions. Use Boolean algebra to simplify the expressions as much as possible before you begin.

(a)  $(A \cdot B) + (\bar{A} \cdot B) + (A \cdot \bar{B}) + (\bar{A} \cdot \bar{B})$

(b)  $[(A \cdot B) + C] \cdot [(A \cdot B) + D]$

(c)  $[(\bar{A} \cdot B) \cdot (A \cdot B)] + (A \cdot B)$

(d)  $(1 + B) \cdot (A \cdot B \cdot C)$

(a)

$$Q = (A \cdot B) + (\bar{A} \cdot B) + (A \cdot \bar{B}) + (\bar{A} \cdot \bar{B})$$

From de Morgan, know

$$\overline{A \cdot B} = \bar{A} + \bar{B}$$

$$\overline{\bar{A} + \bar{B}} = A \cdot B$$

Note:  $A \cdot B + A \cdot \bar{B}$

$$= A \cdot \underbrace{(B + \bar{B})}_1 = A \cdot 1 = A$$

likewise

$$\bar{A} \cdot B + \bar{A} \cdot \bar{B} = \bar{A} \cdot \underbrace{(B + \bar{B})}_1 = \bar{A}$$

$$\therefore Q = A + \bar{A} = 1.$$

$\therefore Q = 1$  always

check:

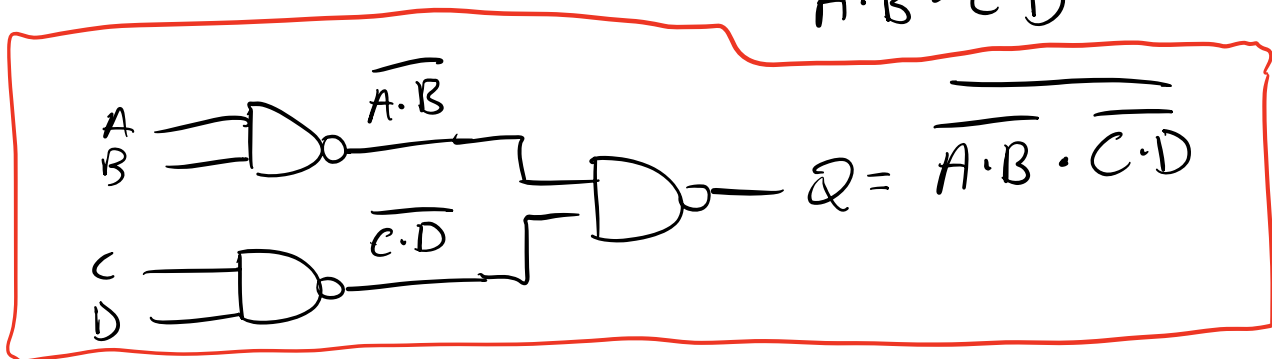
A	B	$\bar{A}$	$\bar{B}$	$A \cdot B$	$A \cdot \bar{B}$	$\bar{A} \cdot B$	$\bar{A} \cdot \bar{B}$	Q
0	0	1	1	0	0	0	1	1
0	1	1	0	0	0	1	0	1
1	0	0	1	0	1	0	0	1
1	1	0	0	1	0	0	0	1

$$(b) \quad Q = [(A \cdot B) + C] \cdot [(A \cdot B) + D]$$
$$= \underbrace{A \cdot B \cdot A \cdot B} + A \cdot B \cdot D + C \cdot A \cdot B + C \cdot D$$
$$= A \cdot A \cdot B \cdot B$$
$$= A \cdot B$$

$$= A \cdot B \cdot (1 + D + C) + C \cdot D$$

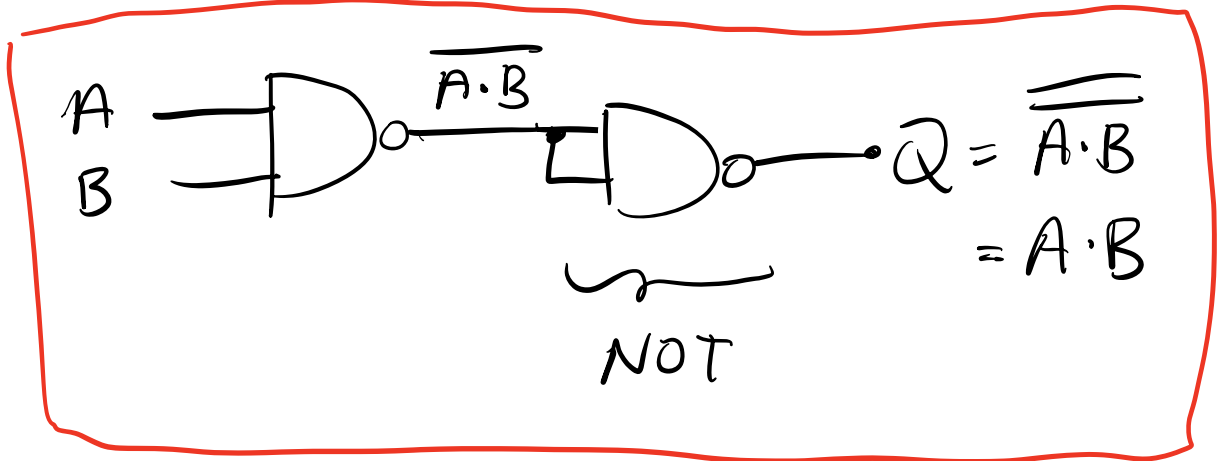
$1 + \text{anything} = 1$

$$\therefore Q = A \cdot B + C \cdot D = \overline{\overline{A \cdot B} + \overline{C \cdot D}}$$
$$= \overline{\overline{A \cdot B} \cdot \overline{C \cdot D}} \quad \text{by deMorgan}$$



$$\begin{aligned}
 (c) \quad Q &= \left[ (\bar{A} \cdot B) \cdot (A \cdot B) \right] + (A \cdot B) \\
 &= \underbrace{\bar{A} \cdot A} \cdot \underbrace{B \cdot B} \\
 &= 0 \cdot B \\
 &= 0 \cdot B = 0
 \end{aligned}$$

$$\therefore Q = 0 + A \cdot B = A \cdot B = \overline{\overline{A \cdot B}}$$



$$(d) \quad Q = (1 + B) \cdot (A \cdot B \cdot C)$$

$$1 + \text{anything} = 1$$

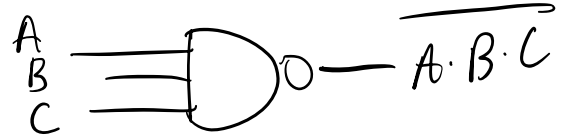
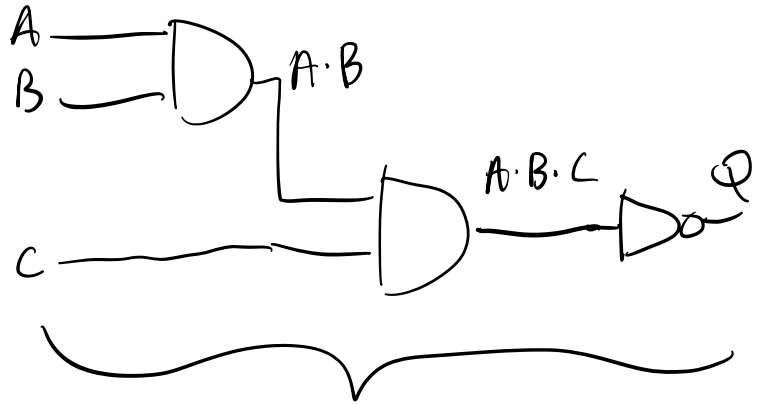
$1 \cdot X = 1$  where X can be anything  
(LO or HI)

$$\therefore Q = 1 \cdot (A \cdot B \cdot C) = A \cdot B \cdot C$$

Note: 3-input NAND truth table

A	B	C	Q
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	0

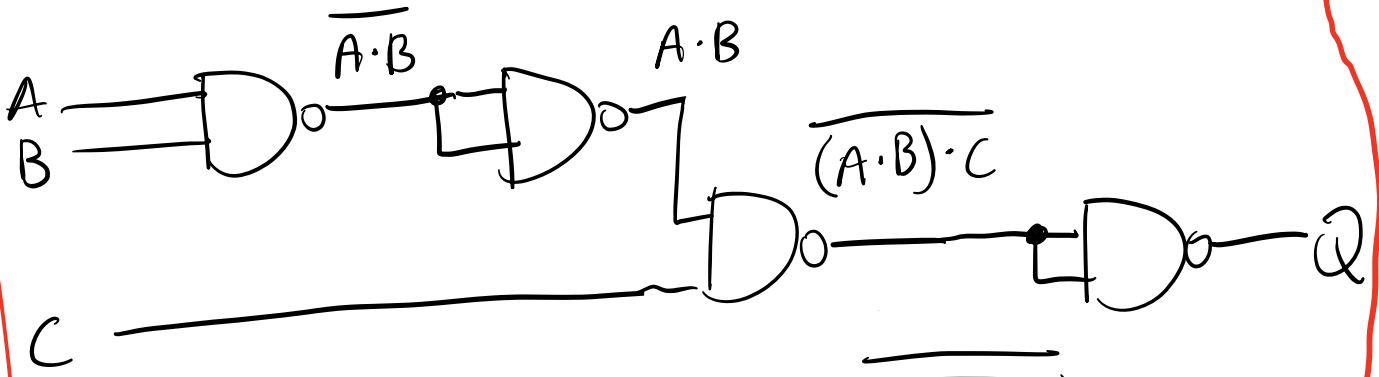
consider



same

A	B	C	$A \cdot B$	$A \cdot B \cdot C$	Q
0	0	0	0	0	1
0	0	1	0	0	1
0	1	0	0	0	1
0	1	1	0	0	1
1	0	0	0	0	1
1	0	1	0	0	1
1	1	0	1	0	1
1	1	1	1	1	0





$$\begin{aligned} Q &= \overline{\overline{(A \cdot B) \cdot C}} \\ &= (A \cdot B) \cdot C \\ &= A \cdot B \cdot C \quad \checkmark \end{aligned}$$